

UNIPAC OPTOELECTRONICS CORPORATION

Spec. No. 233-220-044

Version : 1



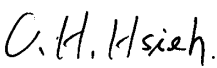
Total pages: 18

Date : 1998/08/07

UP68D01 COLOR TFT-LCD TENTATIVE SPECIFICATION

MODEL NAME: UP68D01

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is subject to change without notice.
Please contact Unipac or its agent for
further information.

Approved by	Checked by	Prepared by
		

UP68D01 Specification Change List

Version	Change Date	Content																																																														
1	1998-7-16	<p>A. Physical Specifications (Page 3)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-bottom: 10px;"> <thead> <tr> <th style="width: 30%;">Item</th> <th style="width: 35%;">Version 0</th> <th style="width: 35%;">Version 1</th> </tr> </thead> <tbody> <tr> <td>Overall dimension (mm)</td> <td>157(W) × 122.5(H)×11.4(D)</td> <td>157.2(W)× 122.6(H)×8.8(D)</td> </tr> <tr> <td>Weight(g)</td> <td style="text-align: center;">340±20</td> <td style="text-align: center;">320±20</td> </tr> </tbody> </table> <p>B. Electrical Specifications</p> <p>2. Absolute maximum ratings (Page 5)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-bottom: 10px;"> <thead> <tr> <th style="width: 60%;">Version 0</th> <th style="width: 40%;">Version 1</th> </tr> </thead> <tbody> <tr> <td>Note 2: STHL,STHR,Q1H,Q2H,OEH, L/R,CPH1~CPH3,STVR,STVL, OEV,CLK,U/D</td> <td>Note 2: STHL,STHR,Q1H,OEH,L/R, CPH1~CPH3,STVR,STVL, OEV,CKV,U/D</td> </tr> </tbody> </table> <p>3. Electrical Characteristics</p> <p>a. Typical operating conditions(GND=AV_{SS}=0) (Page 6)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-bottom: 10px;"> <thead> <tr> <th style="width: 60%;">Version 0</th> <th style="width: 40%;">Version 1</th> </tr> </thead> <tbody> <tr> <td>a. Typical operating conditions (GND=AV_{SS}=0)</td> <td>a. Typical operating conditions (GND=AV_{SS}=0, Note 5)</td> </tr> </tbody> </table> <table border="1" style="width: 100%; border-collapse: collapse; margin-bottom: 10px;"> <thead> <tr> <th style="width: 15%;">Symbol</th> <th style="width: 15%;"></th> <th style="width: 20%;">Version 0</th> <th style="width: 50%;">Version 1</th> </tr> </thead> <tbody> <tr> <td rowspan="3" style="text-align: center; vertical-align: middle;">V_{GLAC}</td> <td style="text-align: center;">Min.</td> <td style="text-align: center;">-</td> <td style="text-align: center;">3.5</td> </tr> <tr> <td style="text-align: center;">Typ.</td> <td style="text-align: center;">6</td> <td style="text-align: center;">5</td> </tr> <tr> <td style="text-align: center;">Max.</td> <td style="text-align: center;">-</td> <td style="text-align: center;">7.5</td> </tr> <tr> <td rowspan="2" style="text-align: center; vertical-align: middle;">V_{GLDC}</td> <td style="text-align: center;">Min.</td> <td style="text-align: center;">-9.5</td> <td style="text-align: center;">-10.5</td> </tr> <tr> <td style="text-align: center;">Max.</td> <td style="text-align: center;">-10.5</td> <td style="text-align: center;">-9.5</td> </tr> <tr> <td style="text-align: center; vertical-align: middle;">V_{iAC}</td> <td style="text-align: center;">Max.</td> <td style="text-align: center;">3.8</td> <td style="text-align: center;">-</td> </tr> <tr> <td rowspan="3" style="text-align: center; vertical-align: middle;">V_{CAC}</td> <td style="text-align: center;">Min.</td> <td style="text-align: center;">-</td> <td style="text-align: center;">3.5</td> </tr> <tr> <td style="text-align: center;">Typ.</td> <td style="text-align: center;">6</td> <td style="text-align: center;">5</td> </tr> <tr> <td style="text-align: center;">Max.</td> <td style="text-align: center;">-</td> <td style="text-align: center;">7.5</td> </tr> <tr> <td style="text-align: center; vertical-align: middle;">V_{CDC}</td> <td style="text-align: center;">Typ.</td> <td style="text-align: center;">1.2</td> <td style="text-align: center;">1.4</td> </tr> </tbody> </table> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 60%;">Version 0</th> <th style="width: 40%;">Version 1</th> </tr> </thead> <tbody> <tr> <td>Note4: STHL,STHR,Q1H,Q2H,OEH, L/R,CPH1~CPH3,STVR,STVL, OEV, CLK,U/D</td> <td>Note 4: STHL,STHR,Q1H,OEH, L/R,CPH1~CPH3,STVR, STVL, OEV,CKV,U/D</td> </tr> <tr> <td></td> <td>Note 5: Be sure to apply GND, V_{CC} and V_{GL} to the LCD first, and then apply V_{GH}.</td> </tr> </tbody> </table>	Item	Version 0	Version 1	Overall dimension (mm)	157(W) × 122.5(H)×11.4(D)	157.2(W)× 122.6(H)×8.8(D)	Weight(g)	340±20	320±20	Version 0	Version 1	Note 2: STHL,STHR,Q1H,Q2H,OEH, L/R,CPH1~CPH3,STVR,STVL, OEV,CLK,U/D	Note 2: STHL,STHR,Q1H,OEH,L/R, CPH1~CPH3,STVR,STVL, OEV,CKV,U/D	Version 0	Version 1	a. Typical operating conditions (GND=AV _{SS} =0)	a. Typical operating conditions (GND=AV _{SS} =0, Note 5)	Symbol		Version 0	Version 1	V _{GLAC}	Min.	-	3.5	Typ.	6	5	Max.	-	7.5	V _{GLDC}	Min.	-9.5	-10.5	Max.	-10.5	-9.5	V _{iAC}	Max.	3.8	-	V _{CAC}	Min.	-	3.5	Typ.	6	5	Max.	-	7.5	V _{CDC}	Typ.	1.2	1.4	Version 0	Version 1	Note4: STHL,STHR,Q1H,Q2H,OEH, L/R,CPH1~CPH3,STVR,STVL, OEV, CLK,U/D	Note 4: STHL,STHR,Q1H,OEH, L/R,CPH1~CPH3,STVR, STVL, OEV,CKV,U/D		Note 5: Be sure to apply GND, V _{CC} and V _{GL} to the LCD first, and then apply V _{GH} .
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(0.8)	0.8	I _{GL}	Typ.	-0.3	-0.41	Max.	(-1)	-1	I _{CC}	Typ.	6	6.5	Max.	(12)	12	I _{DD}	Max.	(20)	20	Symbol		Version 0	Version 1	Remark	Lamp voltage (V _L)	Min.	-	(504)		Typ.	(530)	(560)		Max.	-	(616)		Lamp current (I _L)	Typ.	(7)	(6.2)		Max	-	7		Lamp Starting Voltage (V _s)	Min.	TBD	-	Ta=25 °C	Typ.	-	(650)	Max.	-	(780)	Lamp Starting Voltage (V _s)	Min.	TBD	-	Ta=0 °C	Typ.	-	(850)	Max.	-	(1020)	Symbol		Version 0	Version 1	STH setup time	Min.	35	20	STH hold time	Min.	35	20	CKV pulse width	Typ.	40	50	Clean enable time	Typ.	36	26	VCOM rising time	Max.	(5)	5	VCOM falling time	Max.	(5)	5	RGB delay time	Max.	(1)	1	Item			Version 0	Version 1	Response time		Remark	Note 4	Note 4,6	View angle		Remark	Note 7	Note 6,7	Brightness		Min.	200	250	Typ.	250	280	White chromaticity	x	Min.	0.26	0.25	Typ.	0.31	0.3	Max.	0.36	0.35	y	Min.	0.28	0.3	Typ.	0.33	0.35	Max.	0.38	0.4	Remark		-	-	Note 8
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UP68D01 Specification Change List

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1	1998-7-16	<p>D.Reliability test items:</p> <table border="1" style="width: 100%; border-collapse: collapse; margin: 10px 0;"> <thead> <tr> <th></th> <th colspan="2" style="text-align: center;">Version 0</th> <th colspan="2" style="text-align: center;">Version 1</th> </tr> <tr> <th></th> <th style="text-align: center;">Conditions</th> <th style="text-align: center;">Remark</th> <th style="text-align: center;">Conditions</th> <th style="text-align: center;">Remark</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">Drop (with carton)</td> <td style="text-align: center;">Height: 80cm 1 corner, 3 edges, 6 surfaces</td> <td></td> <td style="text-align: center;">Height: 60cm 1 corner, 3 edges, 6 surfaces</td> <td style="text-align: center;">JIS Z0202</td> </tr> <tr> <td style="text-align: center;">Vibration</td> <td colspan="2" style="text-align: center;">Remark</td> <td colspan="2" style="text-align: center;">Remark</td> </tr> <tr> <td></td> <td colspan="2" style="text-align: center;">Operation JIS C7021, A-10 condition C</td> <td colspan="2" style="text-align: center;">JIS C7021, A-10 condition C</td> </tr> <tr> <td style="text-align: center;">Mech anical shock</td> <td colspan="2" style="text-align: center;">Remark</td> <td colspan="2" style="text-align: center;">Remark</td> </tr> <tr> <td></td> <td colspan="2" style="text-align: center;">Operation JIS C7021, A-7 condition C</td> <td colspan="2" style="text-align: center;">JIS C7021, A-7 condition C</td> </tr> </tbody> </table> <p>Note 7. figure update (Page 9)</p> <p>Update Fig.1 Outline dimension of TFT-LCD module (Page : 11)</p> <p>Update Fig.3 Horizontal display timing range (Page : 13)</p> <p>Update Fig.4(a) Horizontal timing (Page : 14)</p> <p>Update Fig.4(b) Detail horizontal timing (Page : 15)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin: 10px 0;"> <thead> <tr> <th style="width: 50%; text-align: center;">Version 0</th> <th style="width: 50%; text-align: center;">Version 1</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">Note : The rising edge of Q1H and the falling edge of OEV should be synchronized with the falling edge of OEH</td> <td style="text-align: center;">Note : The falling edge of OEV should be synchronized with the falling edge of OEH</td> </tr> </tbody> </table> <p>Update Fig.5 Vertical shift clock timing (Page : 16)</p> <p>Update Fig.6-(a) Vertical timing (From up to down) (Page : 17)</p> <p>Update Fig.6-(b) Vertical timing (From down to up) (Page : 18)</p>		Version 0		Version 1			Conditions	Remark	Conditions	Remark	Drop (with carton)	Height: 80cm 1 corner, 3 edges, 6 surfaces		Height: 60cm 1 corner, 3 edges, 6 surfaces	JIS Z0202	Vibration	Remark		Remark			Operation JIS C7021, A-10 condition C		JIS C7021, A-10 condition C		Mech anical shock	Remark		Remark			Operation JIS C7021, A-7 condition C		JIS C7021, A-7 condition C		Version 0	Version 1	Note : The rising edge of Q1H and the falling edge of OEV should be synchronized with the falling edge of OEH	Note : The falling edge of OEV should be synchronized with the falling edge of OEH
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A.Physical specifications

NO.	Item	Specification	Remark
1	Display resolution(dot)	1152(W) × 234(H)	
2	Active area(mm)	138.24(W) × 103.43(H)	
3	Screen size(inch)	6.8(Diagonal)	
4	Dot pitch(mm)	0.120(W) × 0.442(H)	
5	Color configuration	R.G.B STRIPE	
6	Overall dimension(mm)	157.2(W) × 122.6(H) × 8.8(D)	Note 1
7	Weight(g)	320±20	

Note 1 : Refer to Fig. 1

B.Electrical specifications

1.Pin assignment

a. TFT-LCD panel driving section

Pin no.	Symbol	i/o	Description	Remark
1	GND	-	Ground for logic circuit	
2	V _{CC}	i	Supply voltage for logic control circuit for scan driver	
3	V _{GL}	i	Negative power for scan driver	
4	V _{GH}	i	Positive power for scan driver	
5	STVR	i/o	Vertical start pulse	Note 1
6	STVL	i/o	Vertical start pulse	Note 1
7	CKV	i	Shift clock input for scan driver	
8	U/D	i	UP/DOWN scan control input	Note 1,2
9	OEV	i	Output enable input for scan driver	
10	VCOM	i	Common electrode driving signal	
11	VCOM	i	Common electrode driving signal	
12	L/R	i	LEFT/RIGHT scan control input	Note 1,2
13	Q1H	i	Analog signal rotate input	
14	OEH	i	Output enable input for data driver	
15	STHL	i/o	Start pulse for horizontal scan line	Note 1
16	STHR	i/o	Start pulse for horizontal scan line	Note 1
17	CPH3	i	Sampling and shifting clock pulse for data driver	
18	CPH2	i	Sampling and shifting clock pulse for data driver	
19	CPH1	i	Sampling and shifting clock pulse for data driver	
20	V _{CC}	i	Supply voltage of logic control circuit for data driver	
21	GND	-	Ground for logic circuit	
22	VR	i	Alternated video signal input(Red)	
23	VG	i	Alternated video signal input(Green)	
24	VB	i	Alternated video signal input(Blue)	
25	AV _{DD}	i	Supply voltage for analog circuit	
26	AV _{SS}	-	Ground for analog circuit	

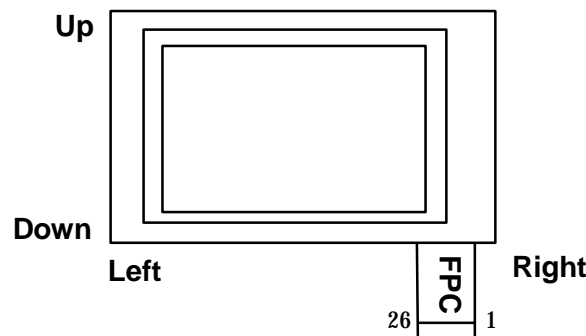
Note 1 : Selection of scanning mode

Setting of scan control input		IN/OUT state for start pulse				Scanning direction
U/D	L/R	STVR	STVL	STHR	STHL	
GND	V _{CC}	OUT	IN	OUT	IN	From up to down, and from left to right.
V _{CC}	GND	IN	OUT	IN	OUT	From down to up, and from right to left.
GND	GND	OUT	IN	IN	OUT	From up to down, and from right to left.
V _{CC}	V _{CC}	IN	OUT	OUT	IN	From down to up, and from left to right.

IN: Input; OUT: Output.

Note 2 : Definition of scanning direction.

Refer to figure as bellow:



b. Backlight driving section(Refer to Fig.1)

No.	Symbol	I/O	Description	Remark
1	HI	i	Power supply for backlight unit (High voltage)	
2	GND	-	Ground for backlight unit	

2. Absolute maximum ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	V _{CC}	GND=0	-0.3	7	V	
	AV _{DD}	AV _{SS} =0	-0.3	7	V	
	V _{GH}	GND=0	-0.3	18	V	
	V _{GL}		-15	0.3	V	
	V _{GH} - V _{GL}		-	31	V	
Input signal voltage	V _i		-0.3	AV _{DD} +0.3	V	Note 1
	V _l		-0.3	V _{CC} +0.3	V	Note 2
	V _{COM}		-2.9	5.2	V	
Operating temperature	Topa		0	60	°C	Ambient temperature
Storage temperature	Tstg		-25	80	°C	Ambient temperature

Note 1: VR, VG, VB

Note 2: STHL, STHR, Q1H, OEH, L/R, CPH1 ~ CPH3, STVR, STVL, OEV, CKV, U/D

3.Electrical characteristics

a. Typical operating conditions (GND=AV_{SS}=0V , Note 5)

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Power supply	V _{CC}	4.8	5	5.2	V	
	AV _{DD}	4.8	5	5.2	V	
	V _{GH}	14.3	15	15.7	V	
	V _{GLAC}	3.5	5	7.5	V _{p-p}	AC component of V _{GL} , Note 1
	V _{GLDC}	-10.5	-10	-9.5	V	DC component of V _{GL}
Video signal amplitude (VR, VG, VB)	V _{iA}	0.4	-	AV _{DD} -0.4	V	Note 2
	V _{iAC}	-	3	-	V	AC component
	V _{iDC}	-	AV _{DD} /2	-	V	DC component
VCOM	V _{CAC}	3.5	5	7.5	V _{p-p}	AC component, Note 3
	V _{CDC}	-	1.4	-	V	DC component
Input signal voltage	H Level	V _{IH}	4	-	V _{CC}	Note 4
	L Level	V _{IL}	0	-	1	

Note 1: The same phase and amplitude with common electrode driving signal(VCOM).

Note 2: Refer to Fig.4-(a)

Note 3: The brightness of LCD panel could be changed by adjusting the AC component of VCOM.

Note 4: STHL,STHR,Q1H,OEH,L/R,CPH1 ~ CPH3,STVR,STVL,OEV,CKV,U/D.

Note 5: Be sure to apply GND , V_{CC} and V_{GL} to the LCD first , and then apply V_{GH} .

b.Current consumption (GND=AV_{SS}=0V)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Current for driver	I _{GH}	V _{GH} =15V	-	0.26	0.8	mA	
	I _{GL}	V _{GL} = -10V	-	-0.41	-1	mA	
	I _{CC}	V _{CC} =5V	-	6.5	12	mA	
	I _{DD}	AV _{DD} =5V	-	10	20	mA	

c.Backlight driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Lamp voltage	V _L	(504)	(560)	(616)	V _{rms}	
Lamp current	I _L	-	(6.2)	7	mArms	
Frequency	F _L	55	60	65	KHz	
Lamp Starting voltage	V _s	-	(650)	(780)	V _{rms}	Note 1
		-	(850)	(1020)	V _{rms}	Note 2

Note 1: Ta = 25 °C

Note 2: Ta = 0 °C

4.AC Timing

a. Timing conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
Rising time	t_r	-	-	10	ns	Note 1
Falling time	t_f	-	-	10	ns	Note 1
High and low level pulse width	t_{CPH}	125	129	133	ns	CPH1~CPH3
CPH pulse duty	t_{CWH}	40	50	60	%	CPH1~CPH3
CPH pulse delay	t_{C12} t_{C23} t_{C31}	30	$t_{CPH}/3$	$t_{CPH}/2$	ns	CPH1 ~ CPH3
STH setup time	t_{SUH}	20	-	-	ns	STHR,STHL
STH hold time	t_{HDH}	20	-	-	ns	STHR,STHL
STH pulse width	t_{STH}	-	1	-	t_{CPH}	STHR,STHL
STH period	t_H	61.5	63.5	65.5	μs	STHR,STHL
OEH pulse width	t_{OEH}	-	10	-	t_{CPH}	OEH
Sample and hold disable time	t_{DIS1}	-	62	-	t_{CPH}	
OEV pulse width	t_{OEV}	-	40	-	t_{CPH}	OEV
CKV pulse width	t_{CKV}	-	50	-	t_{CPH}	CKV
Clean enable time	t_{DIS2}	-	26	-	t_{CPH}	
Horizontal display start	t_{SH}	-	0	-	$t_{CPH}/3$	
Horizontal display timing range	t_{DH}	-	1,152	-	$t_{CPH}/3$	
STV setup time	t_{SUV}	400	-	-	ns	STVL,STVR
STV hold time	t_{HDV}	400	-	-	ns	STVL,STVR
STV pulse width	t_{STV}	-	-	1	t_H	STVL,STVR
Horizontal lines per field	t_V	256	262	268	t_H	Note 2
Vertical display start	t_{SV}	-	3	-	t_H	
Vertical display timing range	t_{DV}	-	234	-	t_H	
VCOM rising time	t_{rCOM}	-	-	5	μs	
VCOM falling time	t_{fCOM}	-	-	5	μs	
VCOM delay time	t_{dCOM}	-	-	3	μs	
RGB delay time	t_{DRGB}	-	-	1	μs	

Note 1: For all of the logic signals.

Note 2: Please don't use odd horizontal lines to drive LCD panel for both odd and even fields simultaneously.

b. Timing diagram

Please refer to the attached drawings, from Fig.2 to Fig.6.

C.Optical specifications (Note 1,Note 2, Note 3)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Response time	Rise	$\theta = 0^\circ$	-	25	50	ms	Note 4,6
	Fall		-	30	60	ms	
Contrast ratio	CR	At optimized viewing angle	60	100			Note 5,6
Viewing angle	Top	$CR \geq 10$	10	-	-	deg.	Note 6,7
	Bottom		30	-	-		
	Left		45	-	-		
	Right		45	-	-		
Brightness	Y_L	$\theta = 0^\circ$	250	280	-	nit	Note 8
White chromaticity	x	$\theta = 0^\circ$	0.25	0.30	0.35		Note 8
	y		0.30	0.35	0.40		
Color temperature	K_L	$\theta = 0^\circ$	-	TBD	-	K	

Note 1. Ambient temperature = 25 °C, and lamp current $I_L=(6.2)mArms$.

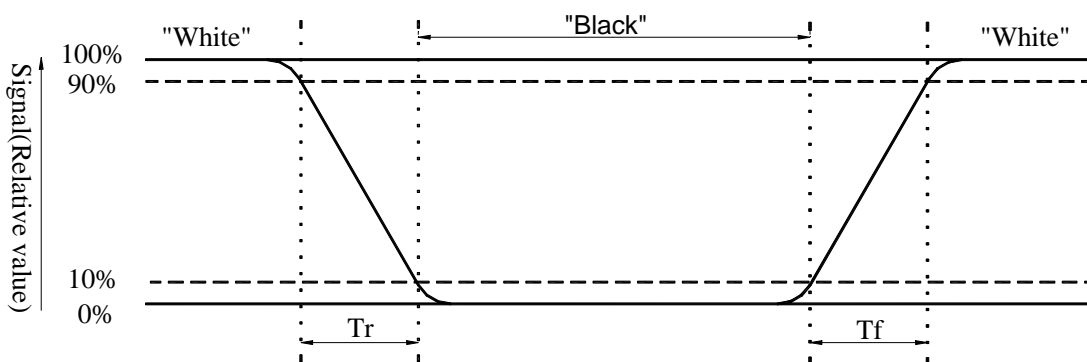
Note 2. To be measured in the dark room.

Note 3. To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation.

Note 4. Definition of response time:

The output signals of photodetector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time),respectively.

The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 5. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Photodetector output when LCD is at "White" state}}{\text{Photodetector output when LCD is at "Black" state}}$$

Note 6. White $V_i = V_{i50} \mp 1.5V$

Black $V_i = V_{i50} \pm 2.0V$

' \pm ' means that analog input signal swings in phase with V_{COM} signal.

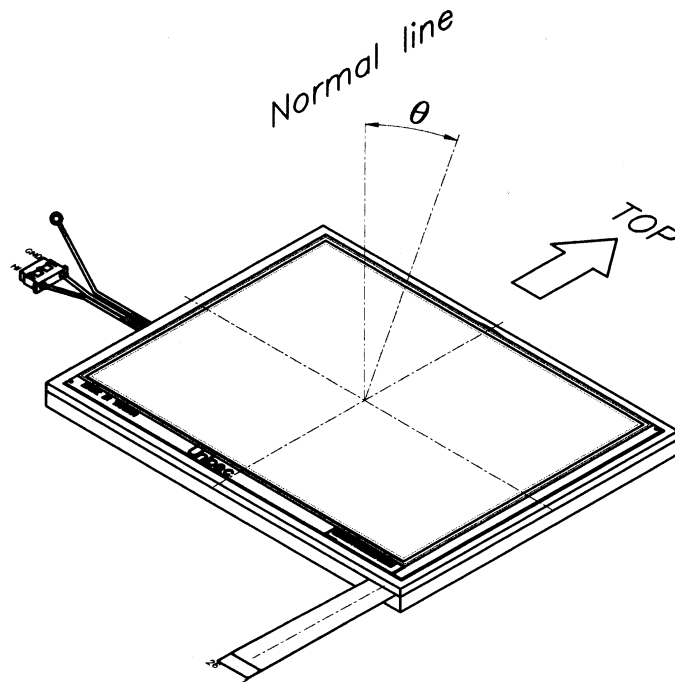
' \mp ' means that analog input signal swings out of phase with V_{COM} signal.

V_{i50} : The analog input voltage when transmission is 50%.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 7. Definition of viewing angle:

Refer to figure as below.



Note 8. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

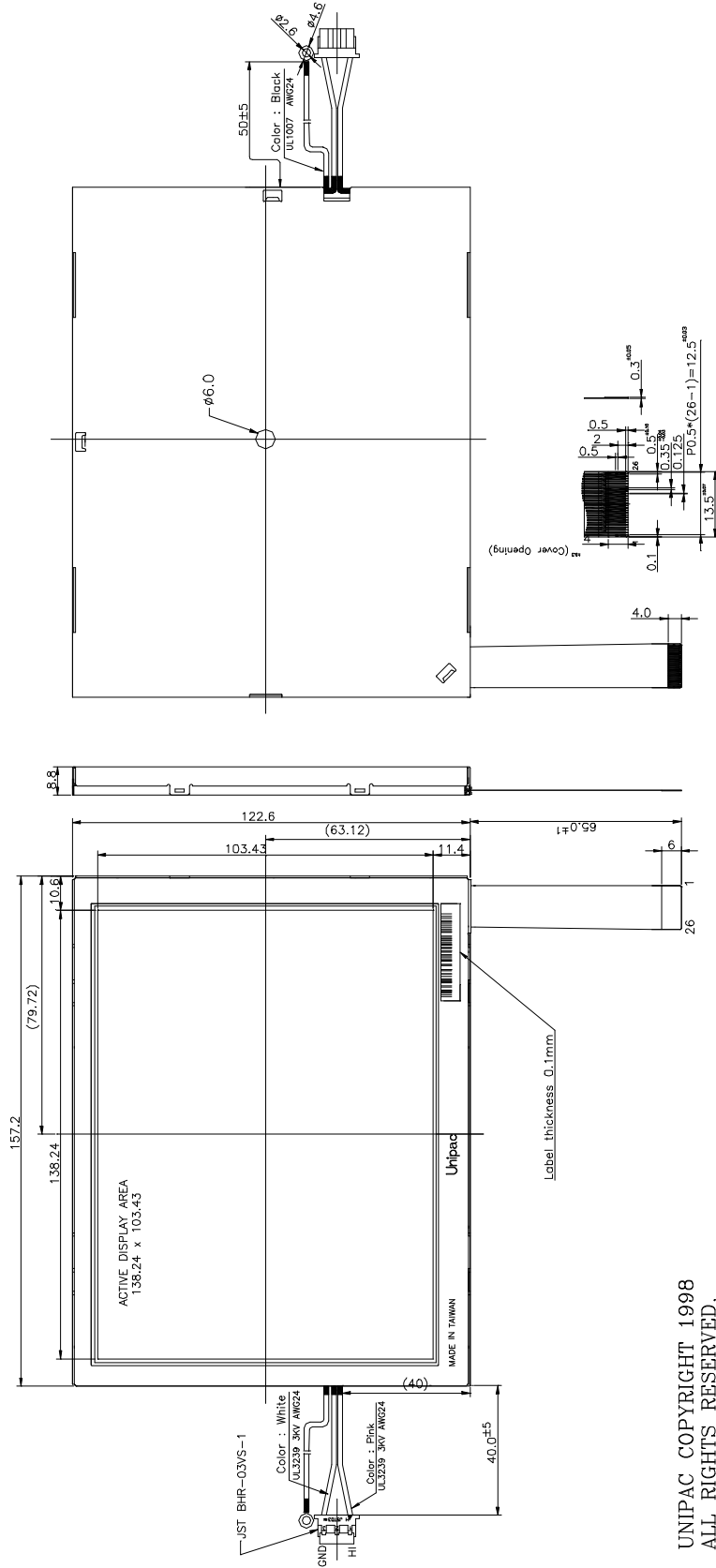
D.Reliability test items:

No.	Test items	Conditions	Remark
1	High temperature storage	Ta = 80°C 240H	
2	Low temperature storage	Ta = -25°C 240H	
3	High temperature operation	Ta = 60°C 240H	
4	Low temperature operation	Ta = 0°C 240H	
5	High temperature and high humidity	Ta = 60°C · 95%RH 240H	Operation
6	Heat shock	-25°C ~ +80°C/50 cycles 2H/cycle	Non-operation
7	Electrostatic discharge	±200V, 200pF(0Ω),once for each terminal	Non-operation
8	Vibration	Frequency range:10 ~ 55Hz Stroke :1.5mm Sweep :10 ~ 55Hz ~ 10Hz 2 hours for each direction of X,Y,Z (6 hours for total)	JIS C7021,A-10 condition A
9	Mechanical shock	100G · 6ms, ±X , ±Y, ±Z 3 times for each direction	JIS C7021,A-7 condition C
10	Vibration (with carton)	Random vibration: 0.015G ² /Hz from 5 ~ 200Hz -6dB/Octave from 200 ~ 500Hz	IEC 68-34
11	Drop (with carton)	Height: 60cm 1 corner,3 edges,6 surfaces	JIS Z0202

Note: Ta: Ambient temperature.

E. Packing form:TBD

- NOTES :
1. General tolerance ± 0.3 .
 2. The bending radius of FPC should be larger than 0.6.
 3. Unit : mm



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Fig.1 Outline dimension of TFT-LCD module

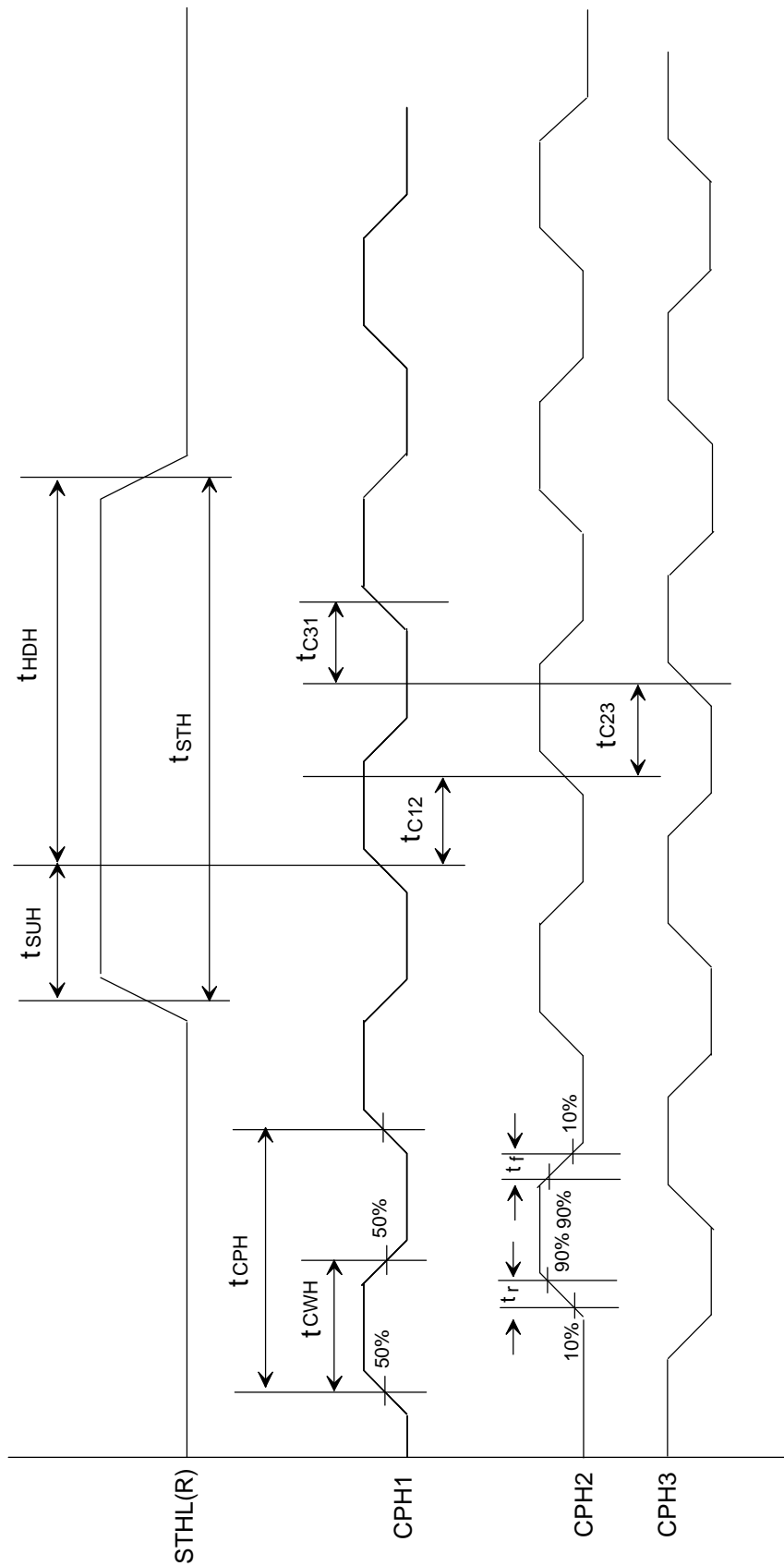


Fig.2 Sampling clock timing

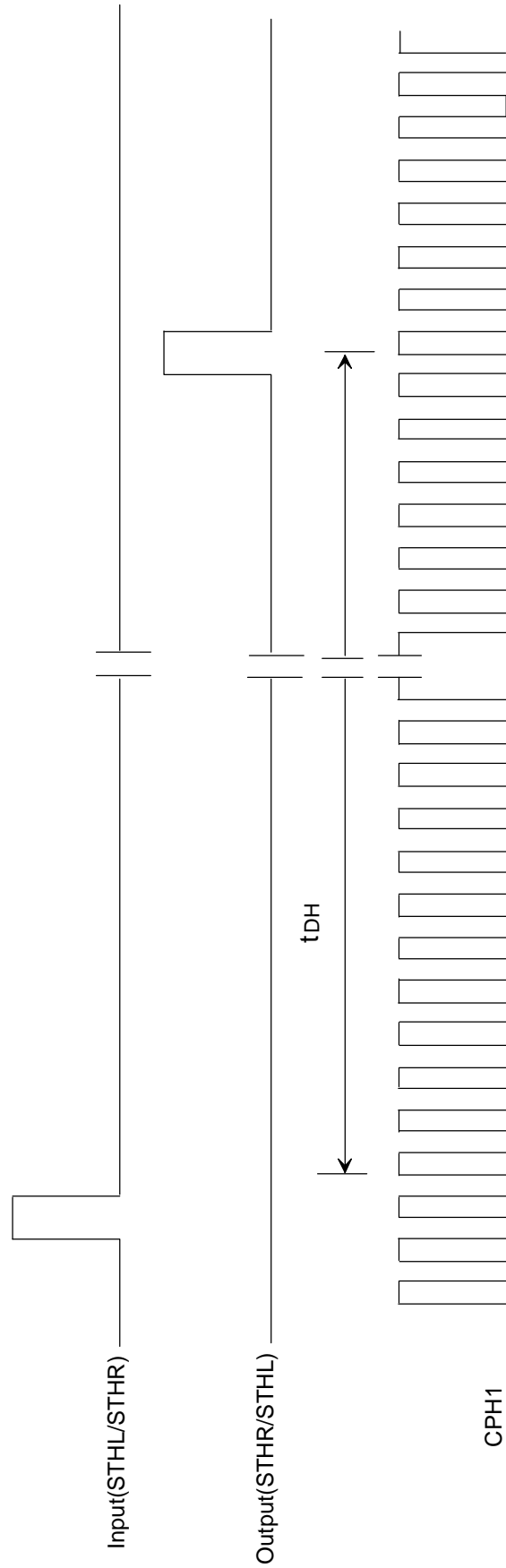


Fig.3 Horizontal display timing range

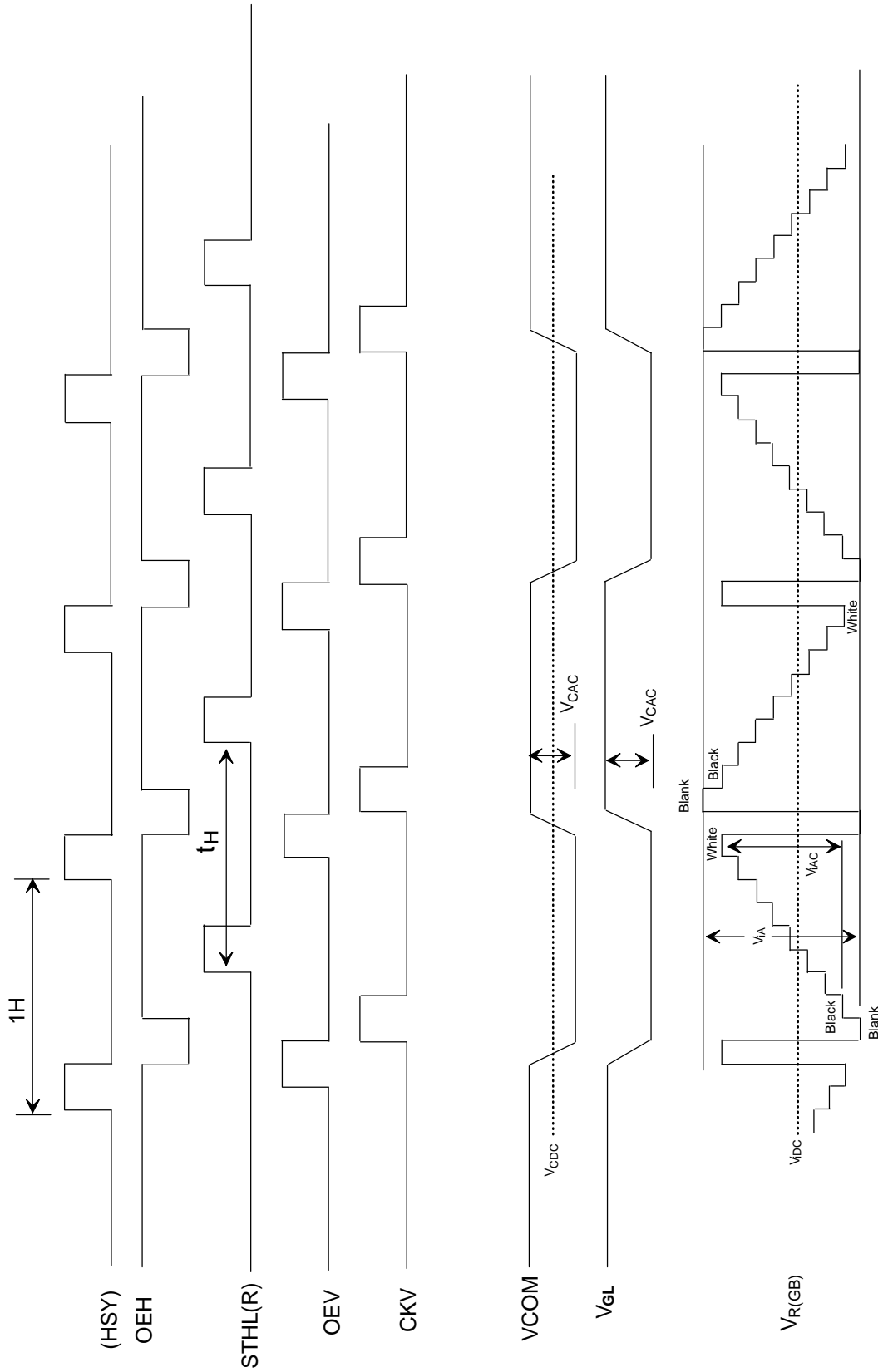
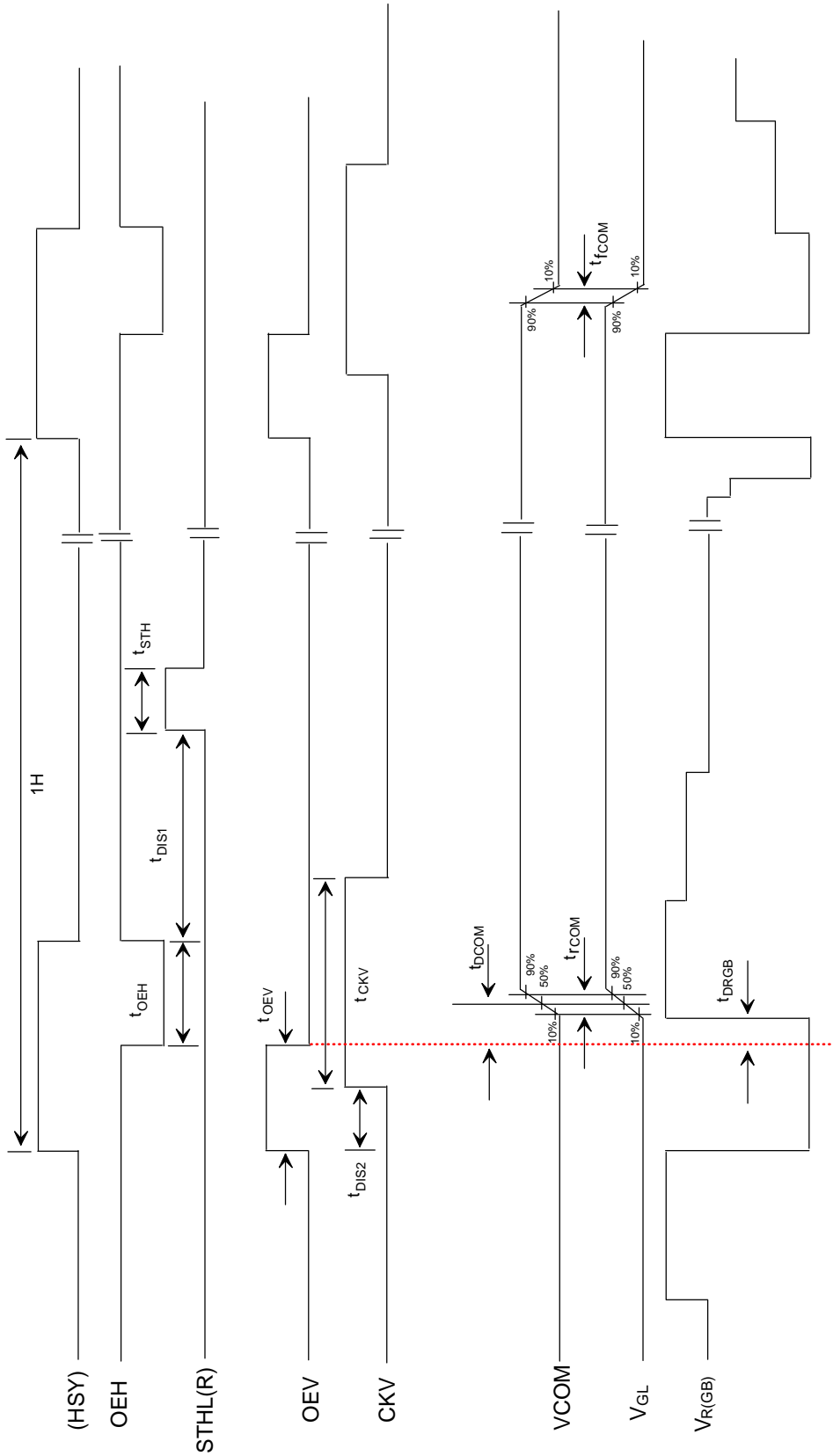


Fig.4-(a) Horizontal timing



Note: The rising edge of Q1H and the falling edge of OEV should be synchronized with the falling edge of OEH

Fig.4-(b) Detail horizontal timing

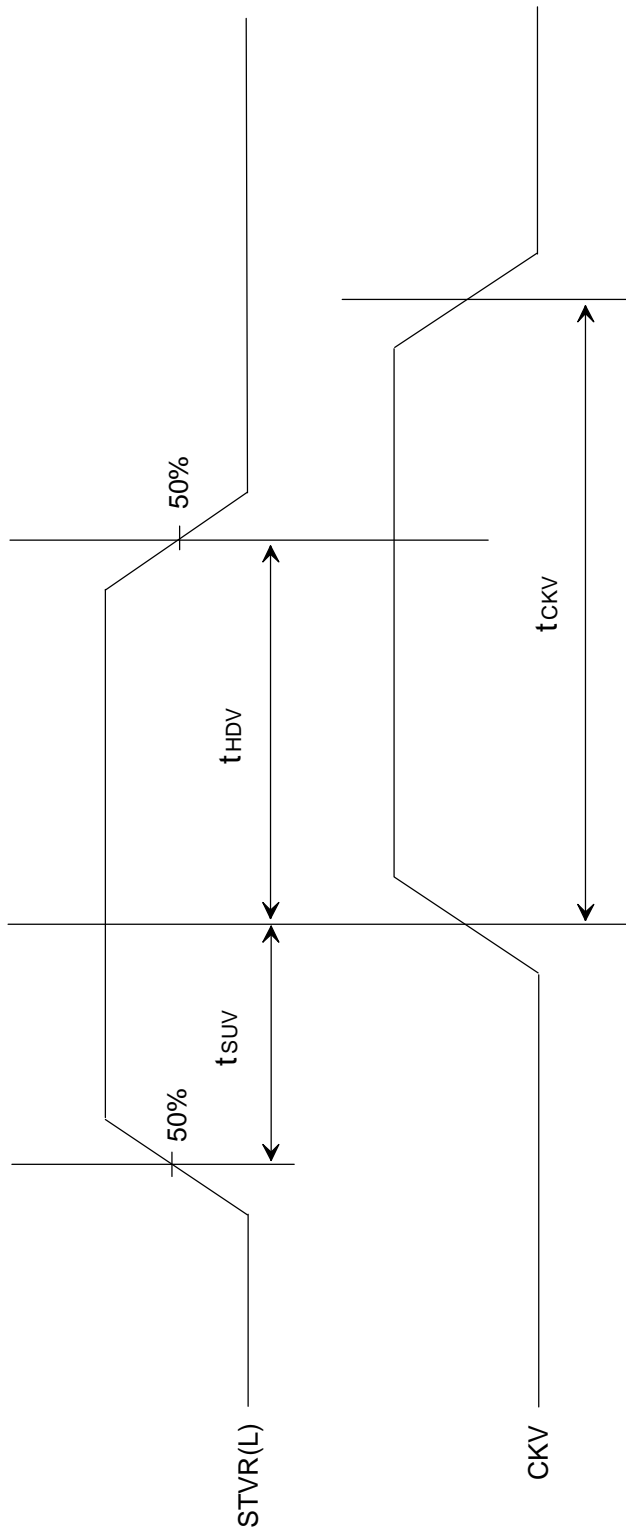


Fig.5 Vertical shift clock timing

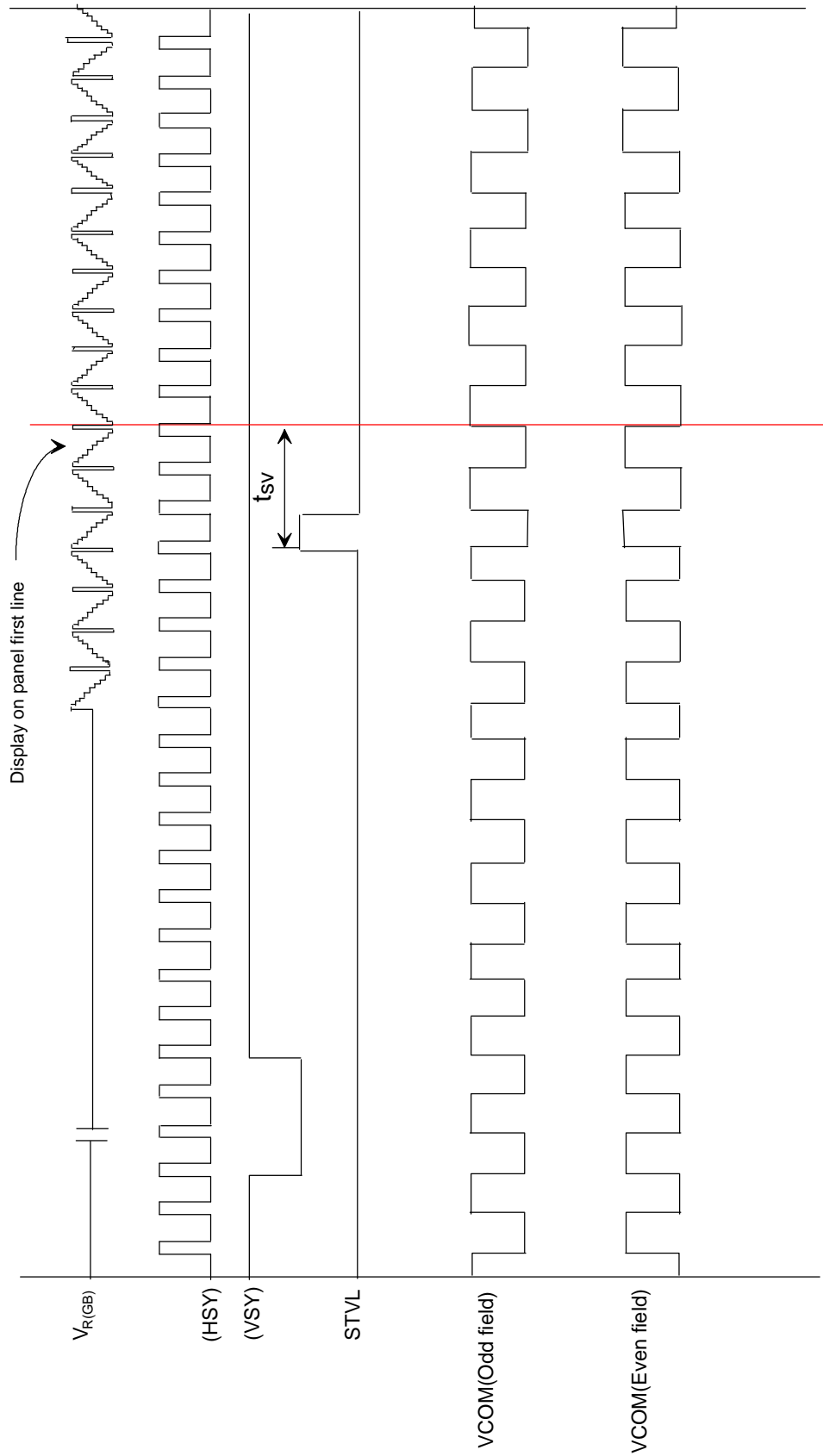


Fig.6-(a) Vertical timing (From up to down)

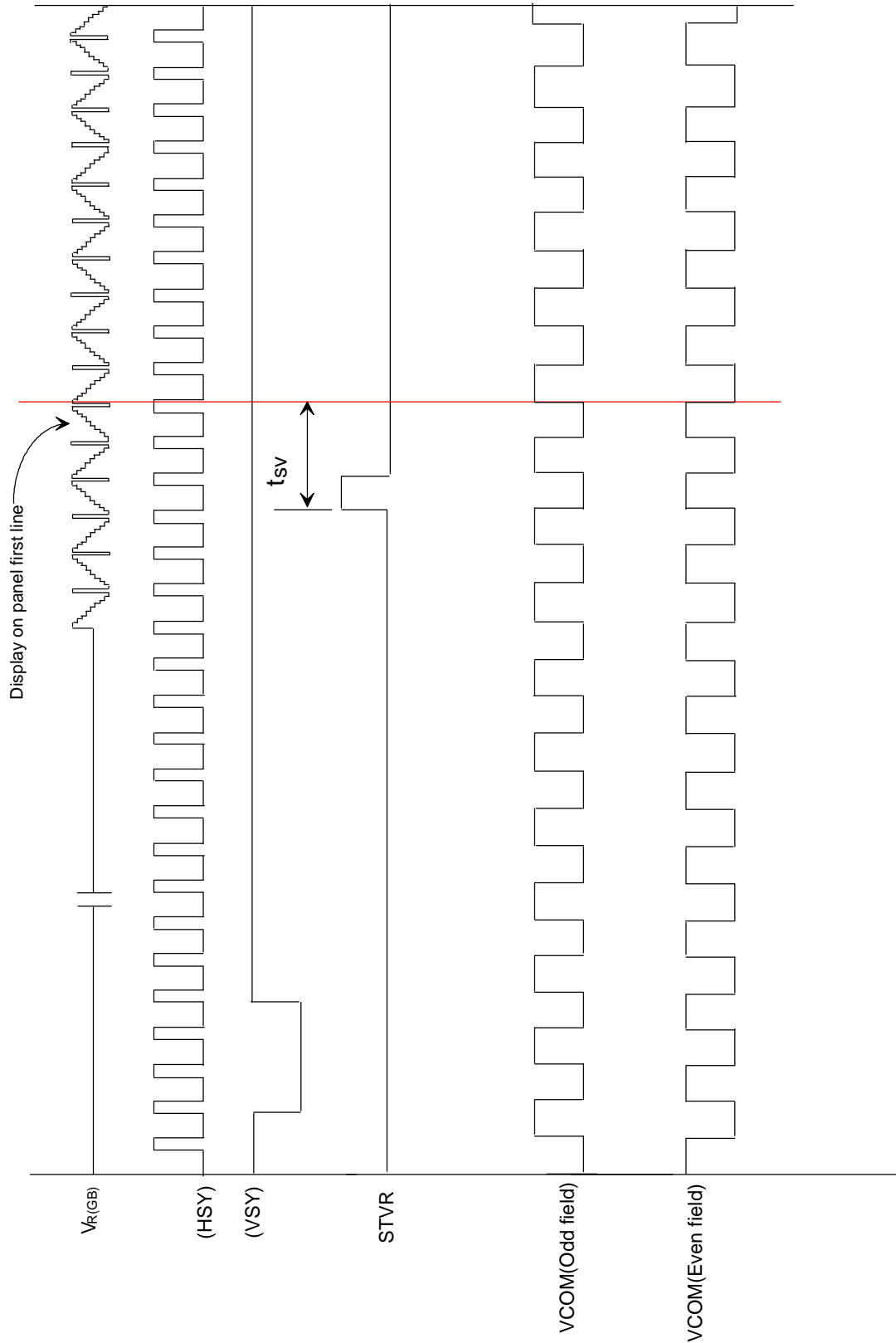


Fig.6-(b) Vertical timing (From down to up)

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- b. In no event will Buyer have rights in partially completed goods.

8. INDEMNIFICATION

Unipac will, at its own expense, assist Buyer with technical support and information in connection with any claim that any parts as shipped by Unipac under this purchase order infringe any valid, enforceable, unexpired R.O.C. patent, copyright, or trademark, provided however, that Buyer (i) gives immediate written notice to Unipac, (ii) permits Unipac to participate and to defend if Unipac requests to do so, and (iii) gives Unipac all needed information, assistance and authority. However, Unipac will not be responsible for infringements resulting from anything not entirely manufactured by Unipac, or from any combination with products, equipment, or materials not furnished by Unipac. Unipac will have no liability with respect to intellectual property matters arising out of products made to Buyer's specifications, code, or designs. Except as expressly stated in this Paragraph 8 or in another writing signed by an authorized officer, Unipac makes no representations and/or warranties with respect to intellectual and/or industrial property and/or with respect to claims of infringement. Except as to claims Unipac agrees in writing to defend, BUYER WILL INDEMNIFY, DEFEND AND HOLD HARMLESS UNIPAC FROM ALL CLAIMS, COSTS, LOSSES, AND DAMAGES (INCLUDING ATTORNEYS FEES) AGAINST AND/OR ARISING OUT OF GOODS SOLD AND/OR SHIPPED HEREUNDER.

9. NO CONFIDENTIAL INFORMATION

Unipac shall have no obligation to hold any information in confidence except as provided in a separate non-disclosure agreement signed by both parties.

10. ENTIRE AGREEMENT

- a. These terms and conditions are the entire agreement between Unipac and Buyer, and no addition, deletion or modification shall be binding on Unipac unless expressly agreed to in a writing signed by an officer of Unipac.
- b. Buyer is not relying upon any warranty or representation except for those specifically stated here.

11. APPLICABLE LAW

This contract and all performance and disputes arising out of or relating to goods involved will be governed by the laws of Taiwan, Republic of China, without reference to conflict of laws principles and excluding the U.N. Convention on Contracts for the International Sale of Goods. Buyer agrees at its sole expense to comply with all applicable laws in connection with the purchase, use or sale of the goods provided hereunder.

12. JURISDICTION AND VENUE

The courts located in Taiwan, Republic of China, will have the sole and exclusive jurisdiction and venue over any dispute arising out of or relating to this contract or any sale of goods hereunder, and Buyer hereby consents to the jurisdiction of such courts.

13. ATTORNEYS' FEES

Reasonable attorneys' fees and costs will be awarded to the prevailing party in the event of litigation involving the enforcement or interpretation of this contract.

Unipac optoelectronics corp.

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