Fuse Bits, Low Byte

For explanation of the Fuse bits, read the data sheet for the particular device. Unprogrammed bits should be set to “1”.

Table 43. Fuse Bits, Low Byte

Fuse Bits	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
ATtiny11	0	0	0	FSTRT	RSTDSBL	CKSEL2	CKSEL1	CKSEL0
ATtiny12	BODLVL	BODEN	SPIEN	RSTDSBL	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATtiny15	BODLVL	BODEN	SPIEN	RTDSBL	1	1	CKSEL1	CKSEL0
ATtiny22	1	1	SPIEN	1	1	1	1	RCEN
ATtiny26	PLLCK	CKOPT	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATtiny28	1	1	1	INTCAP	CKSEL3	CKSEL2	CKSEL1	CKSEL0
AT90S1200	1	1	SPIEN	1	1	1	1	RCEN
AT90S2313	1	1	SPIEN	1	1	1	1	FSTRT
AT90S2323	1	1	SPIEN	1	1	1	1	FSTRT
AT90S2333	1	1	SPIEN	BODLVL	BODEN	CKSEL2	CKSEL1	CKSEL0
AT90S2343	1	1	SPIEN	1	1	1	1	RCEN
AT90S4414	1	1	SPIEN	1	1	1	1	FSTRT
AT90S4433	1	1	SPIEN	BODLVL	BODEN	CKSEL2	CKSEL1	CKSEL0
AT90S4434	1	1	SPIEN	1	1	1	1	FSTRT
AT90S8515	1	1	SIEN	1	1	1	1	FSTRT
AT90S8535	1	1	SPIEN	1	1	1	1	FSTRT
ATmega8	BODLEVEL	BODEN	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega8515	BODLEVEL	BODEN	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega8535	BODLEVEL	BODEN	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega161	1	BOOTRST	SPIEN	BODLVL	BODEN	CKSEL2	CKSEL1	CKSEL0
ATmega163	BODLEV	BODEN	SPIEN	1	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega16	BODLEVEL	BODEN	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega162	CKDIV8	CKOUT	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega169	CKDIV8	CKOUT	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega323	BODLEV	BODEN	1	1	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega32	BODLEVEL	BODEN	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega64	BODLEVEL	BODEN	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
ATmega103	1	1	SPIEN	1	EESAVE	1	SUT1	SUT0
ATmega128	BODLEVEL	BODEN	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
AT89551	1	1	1	1	1	1	1	1
AT89552	1	1	1	1	1	1	1	1
AT86RF401	1	1	1	1	1	1	1	1